

DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled METHOD OF REDUCING ELECTRICAL SHORTS FROM THE BIT LINE TO THE CELL PLATE, the specification of which:

is attached hereto.

X was filed on September 20, 1999, as Application Serial No. 09/399/592.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims.

I acknowledge the duty to disclose information which is material to patentability of the subject matter claimed in this application as "materiality" is defined in Title 37 of the Code of Federal Regulations, § 1.56.

I hereby claim the benefit of any earlier filing date in the United States to which I am entitled under Title 35 of the United States Code, § 120 and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35 of the United States Code, § 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37 of the Code of Federal Regulations, § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

(Application Serial No.)

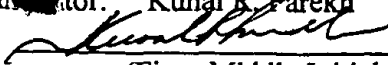
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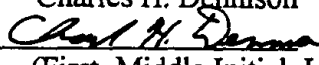
Send correspondence to:

Lia M. Pappas, Mail Stop 525
Micron Technology, Inc.
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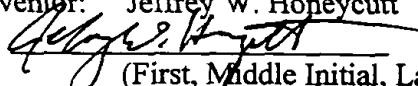
I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18 of the United States Code, § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first or sole inventor: Kunal R. Parekh
Inventor's Signature: 
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Parekh et al.

Serial No.: 09/399,592

Filed: September 20, 1999

For: METHOD OF REDUCING
ELECTRICAL SHORTS FROM THE BIT
LINE TO THE CELL PLATE

Examiner: Unknown

Group Art Unit: 2811

Attorney Docket No.: 4475US (98-1097)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

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1.8(a)(1)(ii)

Joseph A. Walkowski
Typed/printed name of person whose signature is
contained above

**POWER OF ATTORNEY (37 C.F.R. § 1.34(a)), REVOCATION OF PRIOR
POWER OF ATTORNEY (37 C.F.R. § 1.36), REQUEST TO
CHANGE CORRESPONDENCE ADDRESS (37 C.F.R. § 1.33(d))
with STATEMENT PURSUANT TO 37 C.F.R. § 3.73**


Commissioner for Patents
Washington, D.C. 20231

Sir:

As assignee of the entire interest in the above-identified application, all powers of attorney previously given herein are **HEREBY REVOKED** and the following attorneys are hereby appointed to prosecute and transact all business in the U.S. Patent and Trademark Office in connection herewith, and with all continuing and divisional applications hereof.

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09043778-033001
TODDED 8/24/00

By: 
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Chief Patent Counsel, an authorized
representative empowered to grant the
foregoing power of attorney